



The diagram illustrates a USB-to-serial adapter circuit. The central component is the CP2102GMR IC (U3), which interfaces between a USB port and a serial header. The USB port is connected to the IC's VBUS, D+, D-, and GND pins. The serial header is connected to the IC's TXD, RXD, and GND pins. The IC is powered by a 3.3V supply (VCC) and ground (GND). The circuit includes several passive components: capacitors (C1, C2, C3, C4, C5, C6) for decoupling and timing, and resistors (R9, R4, R5) for pull-up and current limiting. The USB symbol is shown in the top left corner.

[illegible]

Pin diagrams for the two 6X2 modules. The top module (pin 37) has pins 1-12 connected to GND, DP3V3, ESDI, ECLK, ECLK, ESDIO, ERST, and RF VCC, RF MOSI, RF SCCK, RF MISO, RF RST. The bottom module (pin 39) has pins 1-12 connected to EIO0, EIO1, EIO2, EIO3, EIO5, ESEL, and RF DIO0, RF DIO1, RF DIO2, RF DIO3, RF DIO5, RF SEL.

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